

ABSTRACT OF THE DISCLOSURES

A common buffer memory control apparatus controls a common buffer memory which is used to store message data items each of which is divided 5 into a plurality of cells based on an asynchronous transfer mode. The common buffer memory control apparatus includes a free block management table for managing whether each of blocks into which the common buffer memory divided is free or used, a 10 block selecting unit for selecting a block of the common buffer memory which is free with reference to the free block management table, and a cell writing control unit for controlling a write operation for cells of a single message data item so that the 15 respective cells of the single message data item are written in the block, selected by the block selecting means, of the common buffer memory.